



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

Applicant: Ohmori
Serial No.: 10/709599
Filed: 05/17/04

Art Unit: Unassigned
Examiner: Unassigned
Atty. Docket: JP920030058

Title: HIGH-VOLTAGE INPUT TOLERANT RECEIVER.

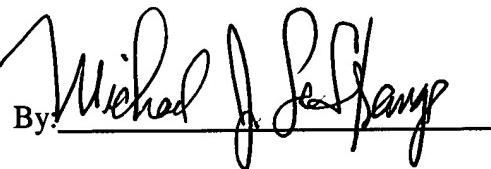
Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Applicants are hereby submitting certified copies of the foreign applications, Japanese Patent Application No. JP2003-137848 filed on May 15, 2003 as specified in 35 U.S.C. § 119(b).

Respectfully submitted,

Date: 7/14/04

By: 

Michael J. LeStrange, Reg. No. 53,207
IP Law Department, 972 E
IBM Corporation
1000 River Street
Essex Junction, VT 05452
Tel.: 802-769-1375

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I, hereby, certify that on the date shown below, this correspondence is being sent by:

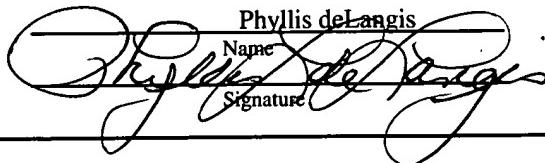
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